

VX855 GPIO Pins

Primary Function	VX855 Ball	IRQ ?	On POR	Str ?	Pull up?	Power Well	Alternate Function	A-phase CL1B Signal	A Dir	B1/B2-phase CL1B signal	B/C Dir	B3/C-phase CL1B signal	B/C Dir	CL1B Description
MSPIDI	AM05	X			10K	+3.3V	GPI0	SPI2_MISO	I	SPI2_MISO	I	SPI2_MISO	I	SPI Port, Master In/Slave Out signal
GPWAKE#	AK18	X			8K	+3.3VSUS	GPI1	SB_WAKE#	I	SB_WAKE#	I	SB_WAKE#	I	Embedded Controller to Host SCI
SDIO0PWOFF	AK25	X			10K	+3.3VSUS	GPI2	WLAN_EN#	O	MSD_PWROFF	O	SD_PWROFF	O	Ext. SD Voltage select (0:3.3, 1:1.8)
SDIO0PWSEL	AM25	X			10K	+3.3VSUS	GPI3		O		O	SD_PWRSEL	O	Ext. SD Power enable
BATLOW#	AJ21	X			8K	+3.3VSUS	GPI4		I	SERIAL_EN	I	SERIAL_EN	I	Jumper (JP1) to disable camera port, enable serial port
EXTSMI#	AM22	X			8K	+3.3VSUS	GPI5		I		I		I	available
INTRUDER#	AF31	X				VBAT	GPI6		I		I		I	available
LID#	AL23	X			8K	+3.3VSUS	GPI7	LID_SW	I	LID_SW	I	LID_SW	I	Output from Lid magnetic switch. Either edge generates SMI
RING#	AL21	X			10K	+3.3VSUS	GPI8	EB_MODE	I		I		I	available
THRM#	AM10				8K	+3.3V	GPI9	PROCHOTB ?	I	EB_MODE	I	EB_MODE	I	Output from E-book mode magnetic switch
GPI10	AL04				10K	+3.3V	SSPISDI	DCONSTAT0	I	DCONSTAT0	I	DCONSTAT0	I	DCON Status lines
GPI11	AM04				10K	+3.3V	SSPISS# / PCI INTA#	DCONSTAT1	I	DCONSTAT1	I	DCONSTAT1	I	
SDIO1PWSEL	AM27				10K	+3.3VSUS	GPI12	SD_PWRSEL	O	SD_PWRSEL	O		O	reserve for WLAN
SDIO1PWOFF	AJ25				10K	+3.3VSUS	GPI13	SD_PWROFF	O	SD_PWROFF	O	WLAN_EN#	O	Power control for WLAN card
PME#	AM18	X			8K	+3.3VSUS	USBD_DET#		I		I		I	available
SMBALRT#	AH20	X			8K	+3.3VSUS		DCONIRQ	I	DCONIRQ	I	SB_WAKE#	I	Embedded Controller to Host SCI
GPO0	AJ10		X	X		+3.3V	SPKR		O		O		O	available
MSPIDO	AL06		X			+3.3V	GPO1	SPI2_MOSI	O	SPI2_MOSI	O	SPI2_MOSI	O	SPI Port, Master Out/Slave In signal
GPO2	AM06		X	X	10K	+3.3V	MSPISS1#		O		O		O	available
MSPISS0#	AK06		X	X	10K	+3.3V	GPO3	SPI2_CS#	O	SPI2_CS#	O	SPI2_CS#	O	SPI Port, CS# for device 0
GPO4	AK04		X			+3.3V	SSPISDO		O		O		O	available
GPO5	AM09		X			+3.3V	CSTATE1		O		O		O	reserve for testing
C4PSTOP#	AK09		X			+3.3V	GPO6	C4PSTOP#	O	C4PSTOP#	O	C4PSTOP#	O	Stop the clocks!
SUSA#	AM23		-			+3.3VSUS	GPO7		O		O		O	reserve
SUSB#	AL24		0			+3.3VSUS	GPO8	MAIN_ON	O	MAIN_ON	O	MAIN_ON	O	THE main power signal
SUSC#	AM24		0			+3.3VSUS	GPO9	SUSC#	O	SUSC#	O	SUSC#	O	Indicates S4 or S5 state
GPO10	AL18		0			+3.3VSUS	USBD_PDN#	USB_PWR_EN	O	USB_PWR_EN	O	USB_PWR_EN	O	Enables power to the USB ports as GPIO, not alternate function.
SDIO2PWSEL	AH19		X			+3.3VSUS	GPO11	WLAN_RESET#	O	WLAN_EN#	O		O	available
SDIO2PWOFF	AL26		X			+3.3VSUS	GPO12	DCONLOAD	O		O		O	available

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GPIO0	AF21	X	1		10K	+3.3VSUS	SMBDT2	DCONSMBDATA	I/O	WLAN_RESET#	I/O	WLAN_RESET#	0	possible WLAN reset signal
GPIO1	AG20	X	1		10K	+3.3VSUS	SMBCK2	DCONSMBCLK	0	DCONLOAD	0	DCONLOAD	0	DCON Load Signal
MSDT	AL20		1		10K	+3.3VSUS	GPIO2		I/O	WLAN_EN# (ECO)	I/O	MSD_PWROFF	0	Internal SD power control
MSCK	AG19		1		10K	+3.3VSUS	GPIO3		I/O		I/O		I/O	reserve
KBDT/ KBC_CPURST#	AM19		1		10K	+3.3VSUS	GPIO4		I/O		I/O		I/O	reserve
KBCK/ A20GATE	AK19		1		10K	+3.3VSUS	GPIO5		I/O		I/O		I/O	reserve
MSPICKL THRMTrip#	AK07 C20		X		10K 10K	+3.3V VCCP	GPIO6 GPIO7	SPI2_SCK THRMTrip#	0 I	SPI2_SCK THRMTrip#	0 I	SPI2_SCK THRMTrip#	0 I	SPI Port, Clock signal Processor thermal overload signal to PM
GPIO8	AK05		X		10K	+3.3V	SSPICKL	DCONBLNK	I	DCONBLNK	I	DCONBLNK	I	DCON Blank signal
GPIO9	AL10		X			+3.3V	PAR	MEM_ID0	I	MEM_ID0	I	MEM_ID0	I	Indicates type of memory chip.
GPIO10	AJ09	X	X	X		+3.3V		WLAN_LED	0	WLAN_LED# (GPIO10)	0	WLAN_LED# (GPIO10)	0	Indicates WLAN activity when driven low
GPIO11	AL11	X	X	X		+3.3V		HDD_LED#	0	HDD_LED#	0	HDD_LED#	0	Indicates disk activity when driven active low.
GPIO12	AM11	X	X	X		+3.3V			I/O		I/O	DCONIRO	I	DCON Interrupt
GPIO13	AK10	X	X	X		+3.3V			I/O		I/O		I/O	available
GPIO14	AF10		X			+3.3V		MEMID1	I	MEMID1	I	MEMID1	I	Indicates type of memory chip.
VGPI01	AA04							SERIAL_ENABLE	I					Not actually present...
VGPI02	T02		?			+3.3V	DISPLCLKI0	Cam_Reset	0	Cam_Reset	0	Cam_Reset	0	Camera reset
VGPI03	R04		?			+3.3V	DISPLCLKO0	Cam_PWREN	0	Cam_PWREN	0	Cam_PWREN	0	Camera power enable
DISPLCLKI1	T01		?			+3.3V	VGPI04	DISPCLKI1	I	DISPCLKI1	I	DISPCLKI1	I	spread spectrum display clock in
DISPLCLKO1	R03		?			+3.3V	VGPI05	DISPCLKO1	0	DISPCLKO1	0	DISPCLKO1	0	display clock output
Dedicated I2C Buses														
SMBDT1	AH23				10K	+3.3VSUS		SMBDT0	I/O	SMBDT0	I/O	SMBDT0	I/O	Clock Gen. I2C bus
SMBCK1	AJ23				10K	+3.3VSUS		SMBCK0	I/O	SMBCK0	I/O	SMBCK0	I/O	
DVPSPD	V01					+3.3V		CAMSMBD	I/O	CAMSMBD	I/O	CAMSMBD	I/O	Camera SCCB bus
DVSPCLK	U03					+3.3V		CAMSMBCLK	I/O	CAMSMBCLK	I/O	CAMSMBCLK	I/O	
CRTSPD	U01					+3.3V		DCONSMBDATA	I/O	DCONSMBDATA	I/O	DCONSMBDATA	I/O	DCON I2C bus (bit-banged)
CRTSPCLK	U02					+3.3V		DCONSMBCLK	I/O	DCONSMBCLK	I/O	DCONSMBCLK	I/O	

An italicized pull-up value means that it is optional under software control. The default is on.
A bold italicized pull-up value means that it is optional and the default is off.