VX855 GPIO Pins

Primary Function	VX855 Ball	IRQ ?	On POR	Str ?	Pull up?	Power Well	Alternate Function	A-phase CL1B Signal	A Dir	B1/B2-phase CL1B signal	B/C Dir	B3/C-phase CL1B signal	B/C Dir
MSPIDI	AM05	X			10K	+3.3V	GPI0	SPI2_MISO	I	SPI2_MISO	I	SPI2_MISO	I
GPWAKE#	AK18	X			8K	+3.3VSUS	GPI1	SB_WAKE#	I	SB_WAKE#	I	SB_WAKE#	I
SDIO0PWOFF	AK25	X			10K	+3.3VSUS	GPI2	WLAN_EN#	O	MSD_PWROFF	O	SD_PWROFF	O
SDIO0PWSEL	AM25	X			10K	+3.3VSUS	GPI3		O		O	SD_PWRSEL	O
BATLOW#	AJ21	X			8K	+3.3VSUS	GPI4		I	SERIAL_EN	I	SERIAL_EN	I
EXTSMI#	AM22	X			8K	+3.3VSUS	GPI5		I		I		I
INTRUDER#	AF31	X				VBAT	GPI6		I		I		I
LID#	AL23	X			8K	+3.3VSUS	GPI7	LID_SW	I	LID_SW	I	LID_SW	I
RING#	AL21	X			*10K*	+3.3VSUS	GPI8	EB_MODE	I		I		I
THRM#	AM10				8K	+3.3V	GPI9	PROCHOTB ?	I	EB_MODE	I	EB_MODE	I
GPI10	AL04				10K	+3.3V	SSPISDI	DCONSTAT0	I	DCONSTAT0	I	DCONSTAT0	I
GPI11	AM04				*10K*	+3.3V	SSPISS# / PCI INTA#	DCONSTAT1	I	DCONSTAT1	I	DCONSTAT1	I
SDIO1PWSEL	AM27				10K	+3.3VSUS	GPI12	SD_PWRSEL	O	SD_PWRSEL	O		O
SDIO1PWOFF	AJ25				10K	+3.3VSUS	GPI13	SD_PWROFF	O	SD_PWROFF	O	WLAN_EN#	O
PME#	AM18	X			8K	+3.3VSUS	USBD_DET#		I		I		I
SMBALRT#	AH20	X			*8K*	+3.3VSUS		DCONIRQ	I	DCONIRQ	I		I
GPO0	AJ10		X	X		+3.3V	SPKR		O		O		O
MSPIDO	AL06		X			+3.3V	GPO1	SPI2_MOSI	O	SPI2_MOSI	O	SPI2_MOSI	O
GPO2	AM06		X	X	10K	+3.3V	MSPISS1#		O		O		O
MSPISS0#	AK06		X	X	10K	+3.3V	GPO3	SPI2_CS#	O	SPI2_CS#	O	SPI2_CS#	O
GPO4	AK04		X			+3.3V	SSPISDO		O		O	HDD_LED#	O
GPO5	AM09		X			+3.3V	CSTATE1		O		O		O
C4PSTOP#	AK09		X			+3.3V	GPO6	C4PSTOP#	O	C4PSTOP#	O	C4PSTOP#	O
SUSA#	AM23		-			+3.3VSUS	GPO7		O		O		O
SUSB#	AL24		0			+3.3VSUS	GPO8	MAIN_ON	O	MAIN_ON	O	MAIN_ON	O
SUSC#	AM24		0			+3.3VSUS	GPO9	SUSC#	O	SUSC#	O	SUSC#	O
GPO10	AL18		0			+3.3VSUS	USBD_PDN#	USB_PWR_EN	O	USB_PWR_EN	O	USB_PWR_EN	O
SDIO2PWSEL	AH19		X			+3.3VSUS	GPO11	WLAN_RESET#	O	WLAN_EN#	O		O
SDIO2PWOFF	AL26		X			+3.3VSUS	GPO12	DCONLOAD	O		O		O

VX855 GPIO Pins

Primary Function	VX855 Ball	IRQ ?	On POR	Str ?	Pull up?	Power Well	Alternate Function	A-phase CL1B Signal	A Dir	B1/B2-phase CL1B signal	B/C Dir	B3/C-phase CL1B signal	B/C Dir
GPIO0	AF21	X	1		10K	+3.3VSUS	SMBDT2	DCONSMBDATA	I/O	WLAN_RESET#	I/O	WLAN_RESET#	O
GPIO1	AG20	X	1		10K	+3.3VSUS	SMBCK2	DCONSMBCLK	O	DCONLOAD	O	DCONLOAD	O
MSDT	AL20		1		***10K***	+3.3VSUS	GPIO2		I/O	WLAN_EN# (ECO)	I/O	MSD_PWROFF	O
MSCK	AG19		1		***10K***	+3.3VSUS	GPIO3		I/O		I/O		I/O
KBDT/ KBC_CPURST#	AM19		1		***10K***	+3.3VSUS	GPIO4		I/O		I/O		I/O
KBCK/ A20GATE	AK19		1		***10K***	+3.3VSUS	GPIO5		I/O		I/O		I/O
MSPICLK	AK07		X		10K	+3.3V	GPIO6	SPI2_SCK	O	SPI2_SCK	O	SPI2_SCK	O
THRMTRIP#	C20		X		*10K*	VCCP	GPIO7	THRMTRIP#	I	THRMTRIP#	I	THRMTRIP#	I
GPIO8	AK05		X		10K	+3.3V	SSPICLK	DCONBLNK	I	DCONBLNK	I	DCONBLNK	I
GPIO9	AL10		X			+3.3V	PAR	MEM_ID0	I	MEM_ID0	I	MEM_ID0	I
GPIO10	AJ09	X	X	X		+3.3V		WLAN_LED	O	WLAN_LED# (GPIO10)	O	WLAN_LED# (GPIO10)	O
GPIO11	AL11	X	X	X		+3.3V		HDD_LED#	O	HDD_LED#	O	SB_WAKE#	I
GPIO12	AM11	X	X	X		+3.3V			I/O		I/O	DCONIRQ	I
GPIO13	AK10	X	X	X		+3.3V			I/O		I/O		I/O
GPIO14	AF10		X			+3.3V		MEMID1	I	MEMID1	I	MEMID1	I
VGPIO1	AA04							SERIAL_ENABLE	I				
VGPIO2	T02		?			+3.3V	DISPLCLKI0	Cam_Reset	O	Cam_Reset	O	Cam_Reset	O
VGPIO3	R04		?			+3.3V	DISPLCLKO0	Cam_PWREN	O	Cam_PWREN	O	Cam_PWREN	O
DISPLCLKI1	T01		?			+3.3V	VGPIO4	DISPCLKI1	I	DISPCLKI1	I	DISPCLKI1	I
DISPLCLKO1	R03		?			+3.3V	VGPIO5	DISPCLKO1	O	DISPCLKO1	O	DISPCLKO1	O
Dedicated I2C Buses													
SMBDT1	AH23				10K	+3.3VSUS		SMBDT0	I/O	SMBDT0	I/O	SMBDT0	I/O
SMBCK1	AJ23				10K	+3.3VSUS		SMBCK0	I/O	SMBCK0	I/O	SMBCK0	I/O
DVPSPD	V01					+3.3V		CAMSMBD	I/O	CAMSMBD	I/O	CAMSMBD	I/O
DVPSPCLK	U03					+3.3V		CAMSMBCLK	I/O	CAMSMBCLK	I/O	CAMSMBCLK	I/O
CRTSPD	U01					+3.3V		DCONSMBDATA	I/O	DCONSMBDATA	I/O	DCONSMBDATA	I/O
CRTSPCLK	U02					+3.3V		DCONSMBCLK	I/O	DCONSMBCLK	I/O	DCONSMBCLK	I/O

An italicized pull-up value means that it is optional under software control. The default is on.
A bold italicized pull-up value means that it is optional and the default is off.

VX855 GPIO Pins

CL1B Description
SPI Port, Master In/Slave Out signal
Embedded Controller to Host SCI
Ext. SD Voltage select (0:3.3, 1:1.8)
Ext. SD Power enable
Jumper (JP1) to disable camera port, enable serial port
available
available
Output from Lid magnetic switch. Either edge generates SMI
available
Output from E-book mode magnetic switch
DCON Status lines
reserve for WLAN
Power control for WLAN card
available
available
available
SPI Port, Master Out/Slave In signal
available
SPI Port, CS# for device 0
Indicates disk activity when driven active low.
reserve for testing
Stop the clocks!
reserve
THE main power signal
Indicates S4 or S5 state
Enables power to the USB ports as GPIO, not alternate function.
available
available

VX855 GPIO Pins

CL1B Description
possible WLAN reset signal
DCON Load Signal
Internal SD power control
reserve
reserve
reserve
SPI Port, Clock signal
Processor thermal overload signal to PM
DCON Blank signal
Indicates type of memory chip.
Indicates WLAN activity when driven low
Embedded Controller to Host SCI
DCON Interrupt
available
Indicates type of memory chip.
Not actually present...
Camera reset
Camera power enable
spread spectrum display clock in
display clock output
Clock Gen. I2C bus
Camera SCCB bus
DCON I2C bus (bit-banged)